



10/ 734300

CJCS

Docket No. : SON-1697/DIV PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No. 7,400,320

Confirmation No. 4716

Inventor: Yoshiharu Nakajima

Patented: July 15, 2008

For: DIGITAL/ANALOG CONVERTER CIRCUIT, LEVEL SHIFT
CIRCUIT, SHIFT REGISTER UTILIZING LEVELSHIFT CIRCUIT,
SAMPLING LATCH CIRCUIT, LATCH CIRCUIT AND LIQUID
CRYSTAL DISPLAY DEVICE INCORPORATING THE SAME

REQUEST FOR CERTIFICATE OF CORRECTION

Certificate

JUL 30 2008

of Correction

MS: CERTIFICATE OF CORRECTION

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

It is respectfully requested that a Certificate of Correction be issued in connection with the above-identified patent. It appears that the errors noted are mistakes of a typographical nature, at least one of which was made by the attorneys for the Applicant. The mistakes were made in good faith and correction thereof would not constitute new matter or require reexamination. Accordingly, two copies of the special Certificate of Correction are attached hereto. It is believed that the errors noted are errors of consequence involving the omission of the sections titled "Related U.S. Application Data" and "Foreign Application Priority Data" and errors in claim language and thus warrant the granting of a Certificate of Correction. A copy of the Official Filing Receipt with the correct U.S. Application Data is enclosed for the convenience of the USPTO.

Please charge Deposit Account No. 18-0013 the \$100.00 fee set forth in 37 C.F.R. § 1.20(a). If additional costs are required, please treat this as authorization to charge such further costs.

Dated: July 28, 2008

Respectfully submitted,

Ronald P. Kananen

Reg. No. 24,104

Christopher M. Tobin

Reg. No. 40,290

JUL 29 2008 SZENDIEI 00000047 180013 7400320

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Washington, D.C. 20036

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UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,400,320

DATED : July 15, 2008

INVENTOR(S) : Yoshiharu Nakajima et al.

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title Page:

Item (62) should be read as follows:

-- Divisional of application No. 09/466,969, filed on December 20, 1999, now Pat. No. 6,664,943 --.

Title Page:

Item (30) should be read as follows:

-- Dec. 21, 1998 (JP).....P10-362283
Feb. 1, 1999 (JP).....P11-023382
Feb. 1, 1999 (JP).....P11-023383
Feb. 1, 1999 (JP).....P11-023384
Oct. 21, 1999 (JP).....P11-299188 --.

Column 32, Line 54:

"A" should be deleted.

Column 33, Line 41:

"firm" should read -- film --.

Column 33, Line 46:

"A" should be deleted.

Column 46, Line 4:

"A" should be deleted.

MAILING ADDRESS OF SENDER:

Customer No. 23353
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PATENT NO. 7,400,320

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CERTIFICATE OF CORRECTION**

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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/734,300	12/15/2003	2871	1122	SON-1697/DIV	31	30	5

CONFIRMATION NO. 4716

23353
 RADER FISHMAN & GRAUER PLLC
 LION BUILDING
 1233 20TH STREET N.W., SUITE 501
 WASHINGTON, DC 20036

FILING RECEIPT



OC000000012311274

Date Mailed: 04/08/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Yoshiharu Nakajima, Kanagawa, JAPAN;
 Toshikazu Maekawa, Kanagawa, JAPAN;

Domestic Priority data as claimed by applicant

This application is a DIV of 09/466,969 12/20/1999 PAT 6,664,943

Foreign Applications

JAPAN P10-362283 12/21/1998
 JAPAN P11-023383 02/01/1999
 JAPAN P11-023384 02/01/1999
 JAPAN P11-023382 02/01/1999
 JAPAN P11-299188 10/21/1999

If Required, Foreign Filing License Granted: 04/07/2004

Projected Publication Date: 07/15/2004

Non-Publication Request: No

Early Publication Request: No

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Title

Digital/analog converter circuit, level shift circuit, shift register utilizing level shift circuit, sampling latch circuit, latch circuit and liquid crystal display device incorporating the same

Preliminary Class

349

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Title 37, Code of Federal Regulations, 5.11 & 5.15**

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